

MPLSSD&AINETWORLD23

★18/20APRIL



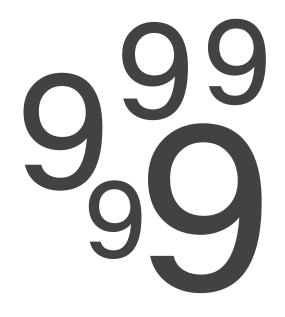
MPLSSD&AINETWORLD23 ★18/20 APRIL

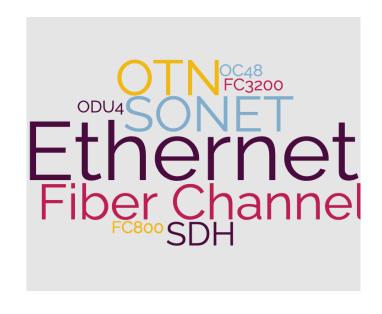
Wavelength Services delivered over IP/MPLS ... Why Now?

Christian Schmutzer, Distinguished Engineer

Wavelengths (Private Lines) are Premium Services







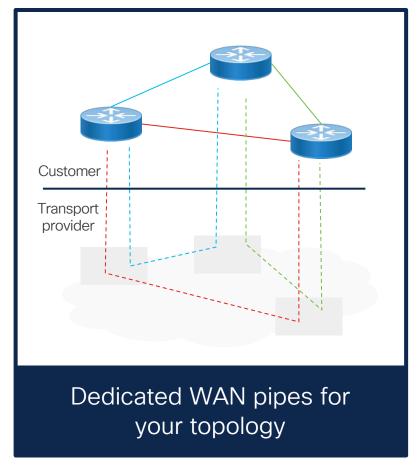
High bandwidth

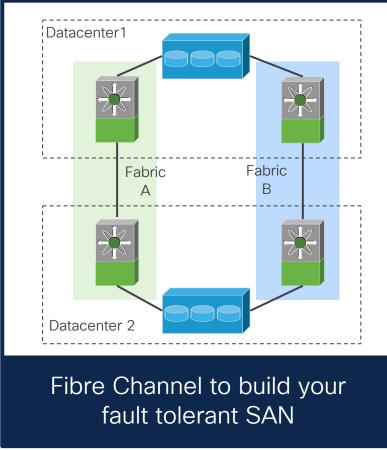
High availability

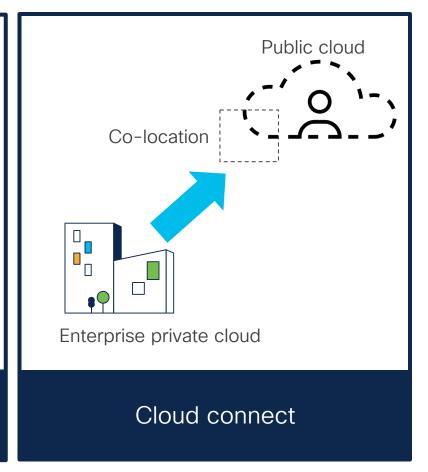
Diverse payloads



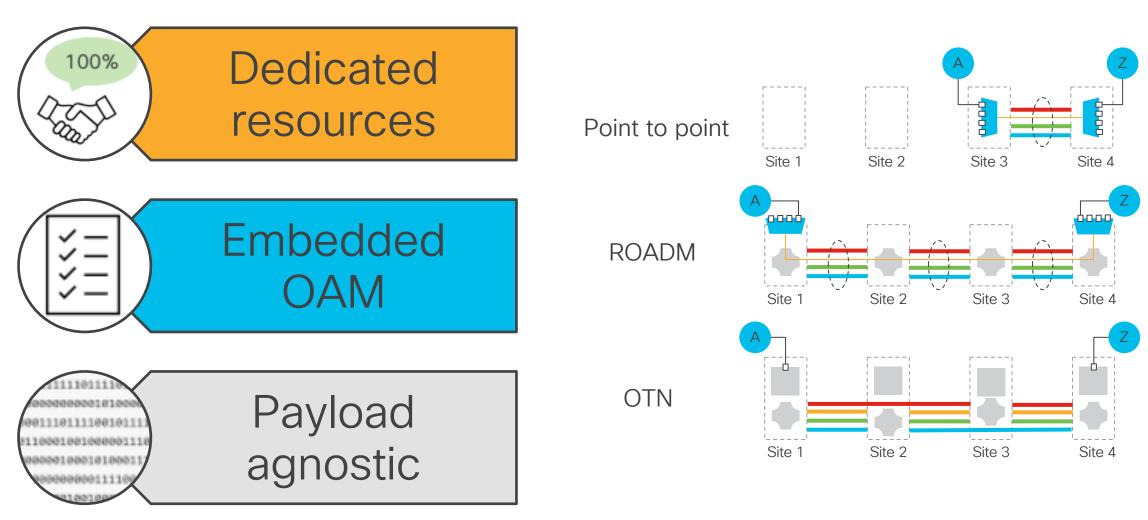
When Only the Very Best will Do...







Wavelengths, TDM have been the Gold Standard



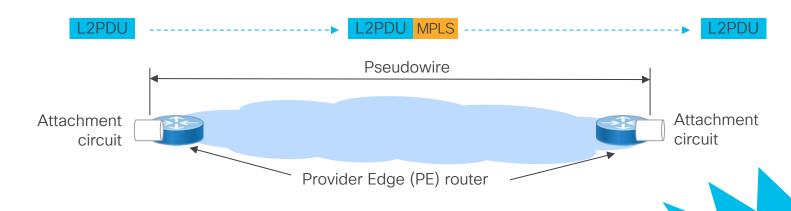




Meanwhile the Reality in Packet Networks

Brilliant Idea back in 2001 (RFC3985 and RFC3915)

Transport L2 payload over packet networks using a "pseudo" wire service



PROs

- One network for L2 and L3 services
- Efficient use of network bandwidth
- Simple and scalable

CONs & CONCERNS

- Ethernet payloads only
- OAM
- Bandwidth commitment
- Load-balancing

Conclusion.

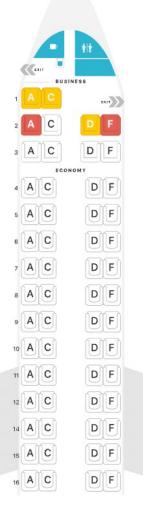
Good for many services
but NOT for premium
private line use cases



Wavelength is a Private Jet



- Always reserved for your team
- Runs on a schedule
- Put whoever you want in those seats



Packet is Like Driving in LA



Shared Bandwidth (rush hour)



Asymmetric routes & latency

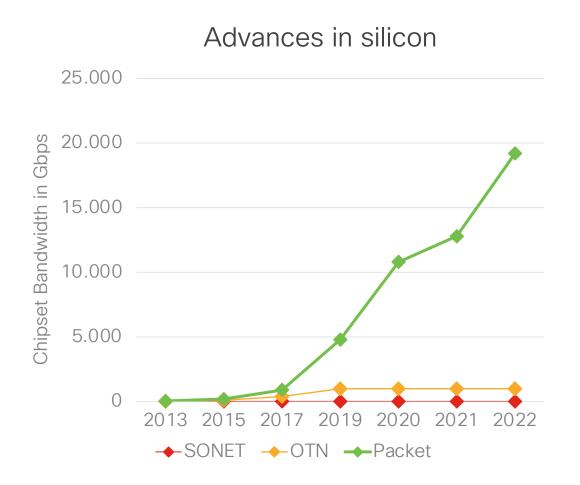




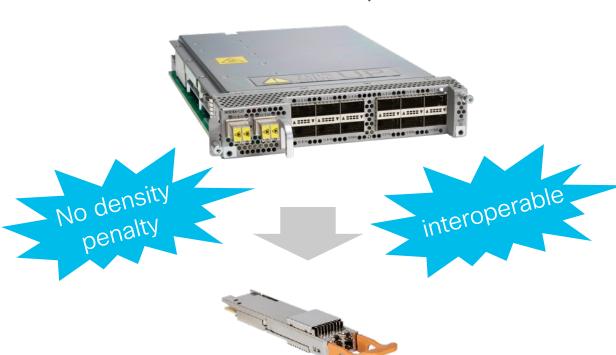
Source: Seatguru

Source: google maps

Massive Shifts in Economics of Routing



Coherent transceivers eliminate the need for DWDM transponders





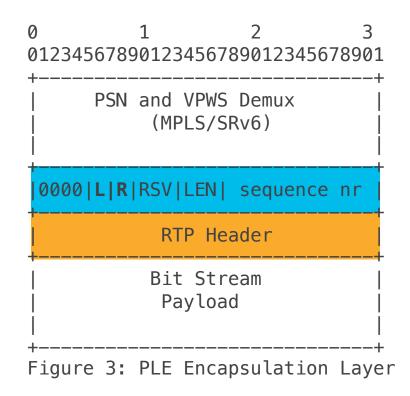
Private Line Emulation (PLE)

... addressing Multi-Protocol support and Transparency



Innovations on Ingress: Bits to Packets & OAM

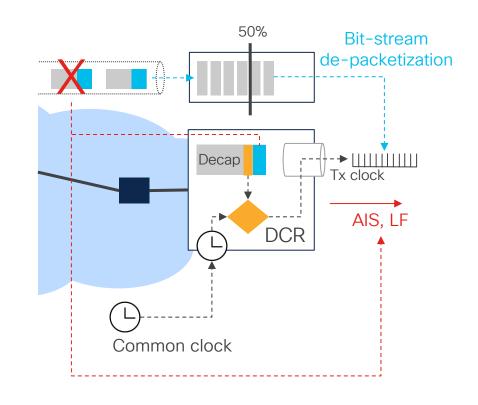
- Encapsulation of bit-stream into packets
 - Extend idea from RFC 4553 (SATOP) beyond T1/E1 to Ethernet, Fibre Channel, OCn/STMn, ...
 - Carry RTP timestamps for clock recovery
- Embedded OAM via PW control word
 - Client faults → L bit set
 - Rx pseudowire network fault → R bit set



Source: draft-schmutzer-pals-ple

Tougher Problems to Solve on Egress

- De-jitter buffer
 - Packet Delay Variation (PDV)
 - Re-sequencing
- Differential clock recovery (DCR)
 - Compare common clock and RTP timestamps to recover client clock
- Client fault indication
 - Too many packets lost
 - If L bit is set





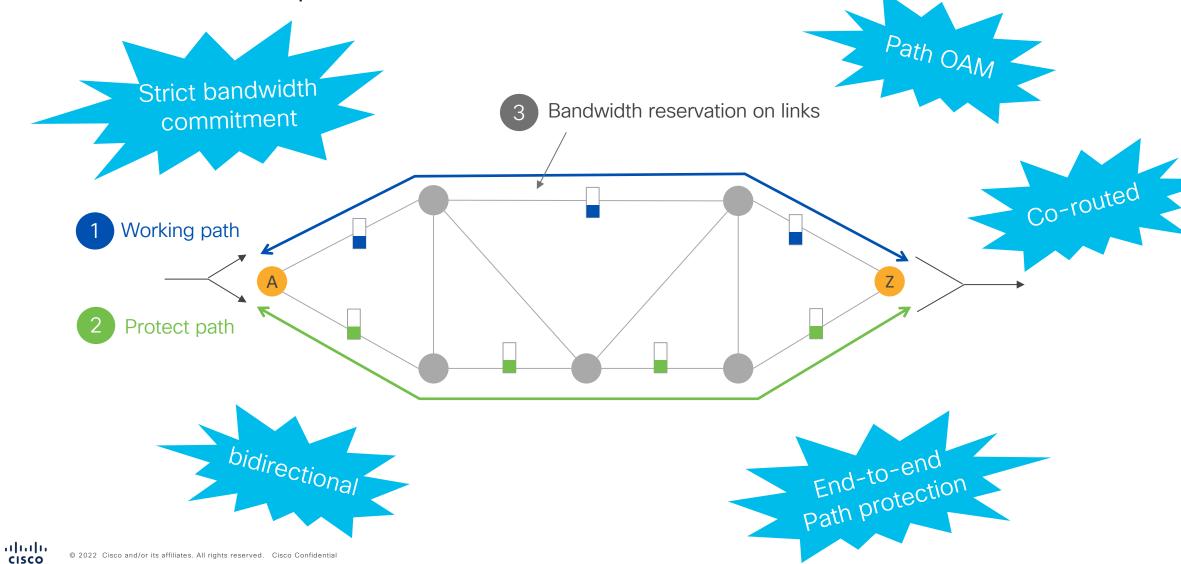
AIS ... Alarm Indication Signal LF ... Local Fault

Circuit-style Segment Routing (CS-SR)

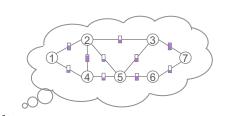
... Solving the LA Rush Hour Problem

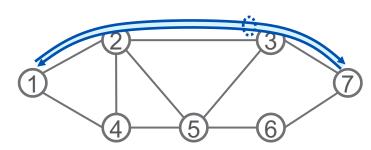


Premium Transport for Premium Services

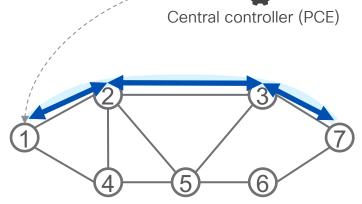


CS-SR provides the Solution

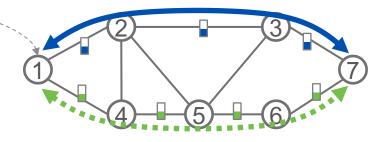




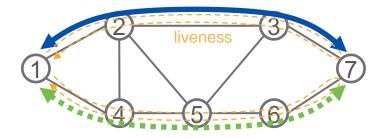
3. Forward and retourn SR policies are routed co-routed



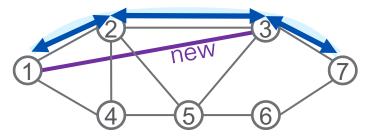
1. Adj-SIDs provide a non-ECMP path with guaranteed latency



5. Guaranteed bandwidth by central reservation bookkeeping



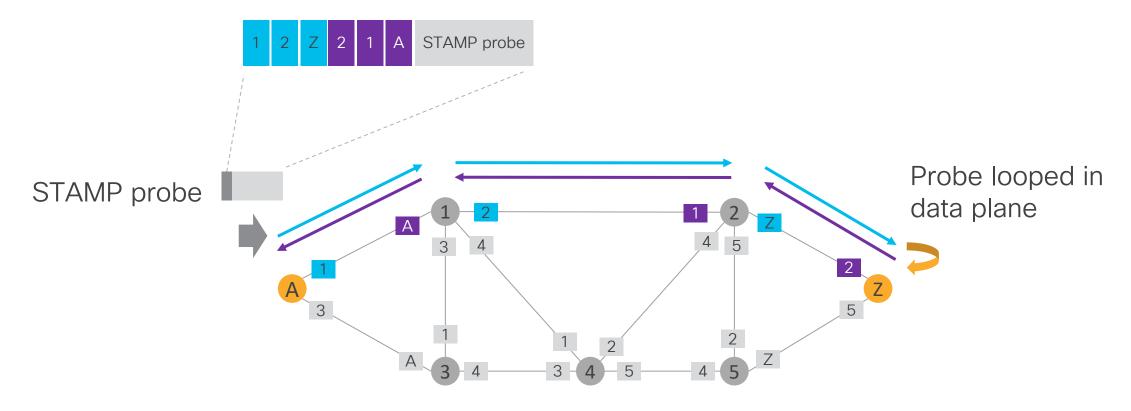
4. Path protection with disjoint protect path



2. The persistent path is independent from network events and control-plane status



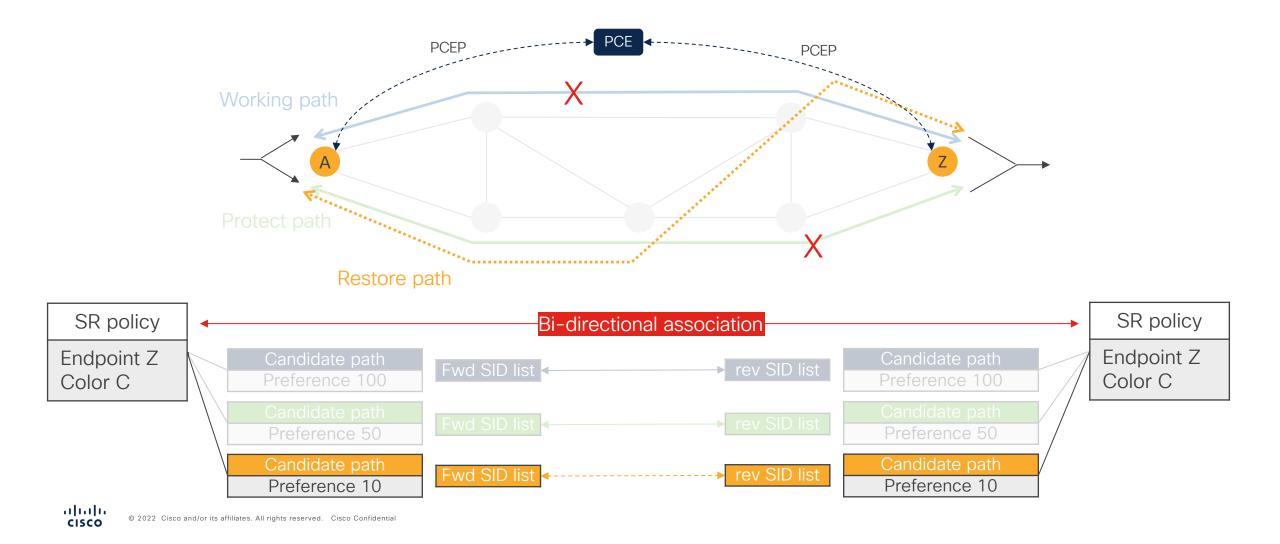
CS-SR with STAMP adds OAM to Transport



- Bidirectional Liveness and performance measurement via a single protocol (STAMP)
- Loopback probes with exact reverse path encoded



With CS-SR, Dynamic Recovery from Double Failures



Make the Network Suit your Needs

Service overlay

PLE

- Bit transparent
- Other payloads: FibreChannel, ...

EVPN-VPWS

- Ethernet only
- No special hardware required

Underlay transport

Circuit-style SR (CS-SR)

- bi-directional path with bandwidth guarantees
- End-to-end path protection and restoration

connection-oriented

"classic" SR

Scale & simplicity

connection-less



From Closed Optical to Open Packet Private Lines



Big Routers with standard interfaces





